

You make possible



Catalyst 9000 Family

An Architectural View

Muhammad A Imam, Technical Marketing

BRKARC-2035



Barcelona | January 27-31, 2020



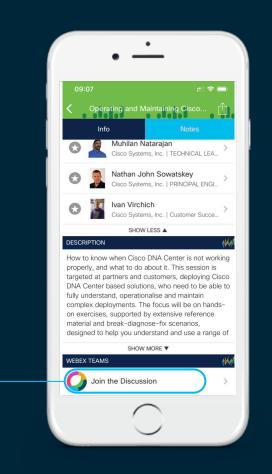
Cisco Webex Teams

Questions?

Use Cisco Webex Teams to chat with the speaker after the session

How

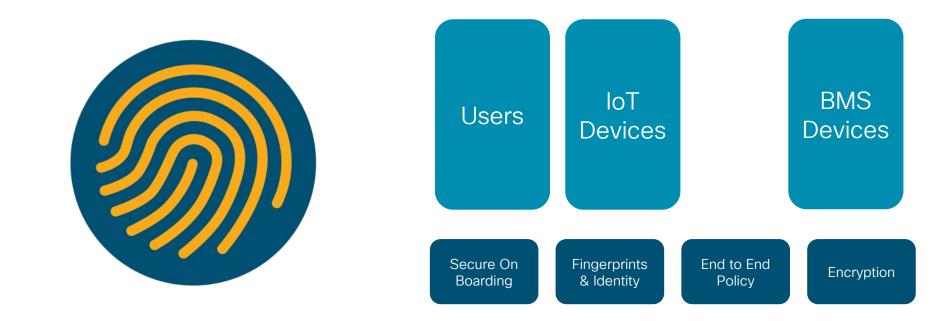
- 1 Find this session in the Cisco Events Mobile App
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- 3 Install Webex Teams or go directly to the team space
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Campus of the Future

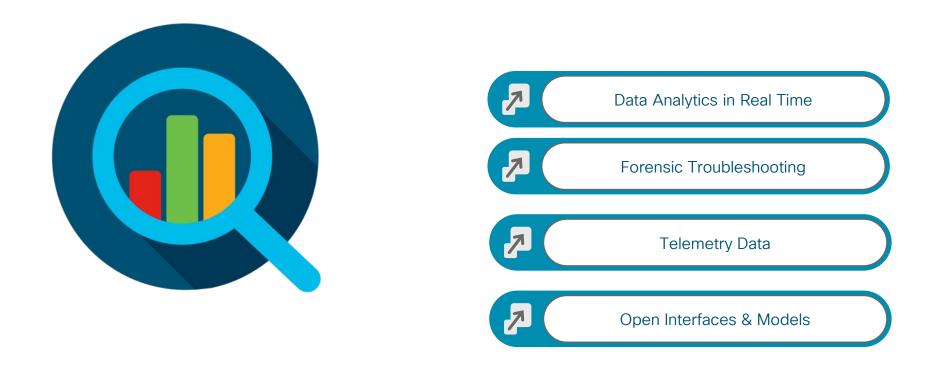
New Trends Drive New Requirements for the Network

What does this really mean for Campus of the Future?



Design your Networks for End to End Security & Segmentation

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Data is Essential to Operate & Manage the Networks

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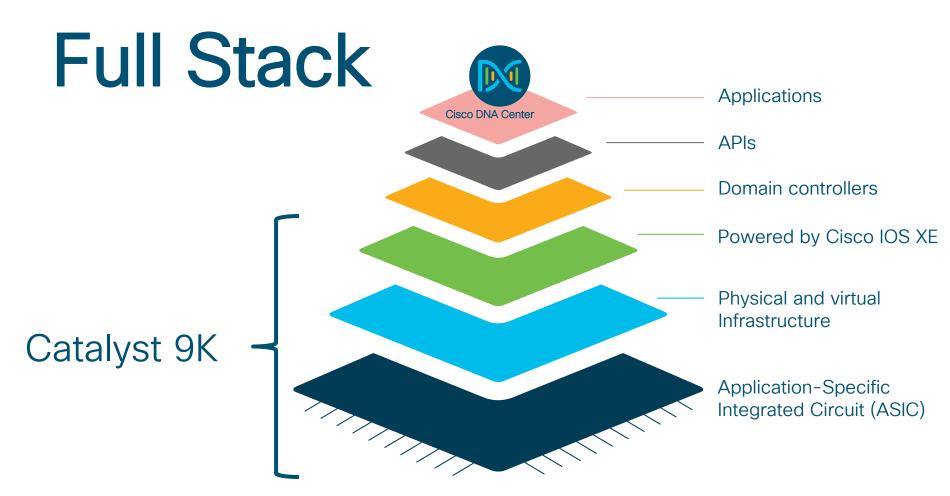


For Campus of the Future

Always ON 99.9999999 % Availability

Always Available is the New Requirement

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Catalyst 9K Family



Built for Campus of the Future

The goal of this session is to give you an architectural view of Catalyst 9000 family, and how it enables Campus of the Future

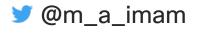
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Muhammad A Imam

Sr. Manager Technical Marketing



CCIE#27739



muimam@cisco.com

Muhammad currently works as a Director **Technical Marketing for Enterprise Networks** Group. Muhammad joined Cisco in 2008 and has around 15 years of experience in the networking industry. Currently he leads a team of Catalyst Products TMEs. He is one of the first TMEs to work on Catalyst 3850/3650 and Catalyst 9000 Family and has worked on all Catalyst Switching products over the years. He also contributes to Enterprise Network designs and Next Generation Platform Architectures. In the past he has held roles in Development, Test and Support of different products ranging from Routers, Switches, Firewalls, etc.

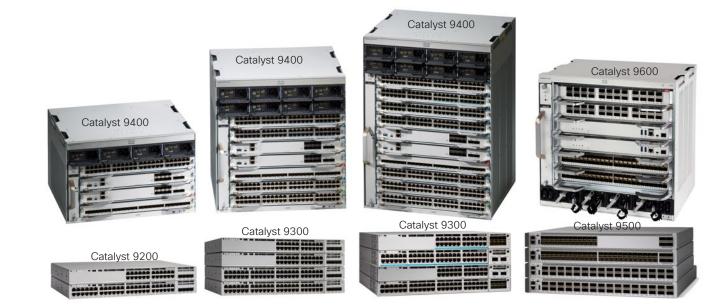
Muhammad holds a Masters degree in Electrical & Computer Engineering. He also maintains a CCIE #27739 in Routing and Switching.

Catalyst 9000 Family

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Catalyst 9000 (9K) – A Growing Family



IOS-XE 16 Common Software Architecture

UADP 2.0 Common Hardware Architecture

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The Latest Addition - Catalyst 9600



Modular Campus Core – Modernizing the Catalyst 6K

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And there are a couple more Catalyst 9K

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The First Catalyst Wireless LAN Controller



BRKEWN-2670 - Introduction to Cisco Catalyst 9800 Wireless Controller

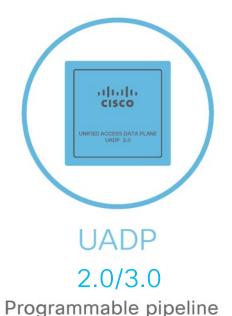
Catalyst 9800 Powered by IOS XE

Building Blocks





Catalyst 9K – Common Building Blocks



Flexible tables



Open and Extensible IOS-XE

> Model-Driven APIs Streaming telemetry

Building Blocks to Face the challenges of Campus of the Future

UADP - Next Generation of ASIC Innovation



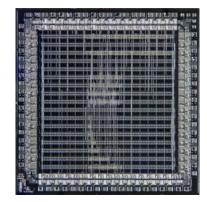
16nm Technology with latest ASIC

Embedded Microprocessors Up to 36MB Packet Buffer



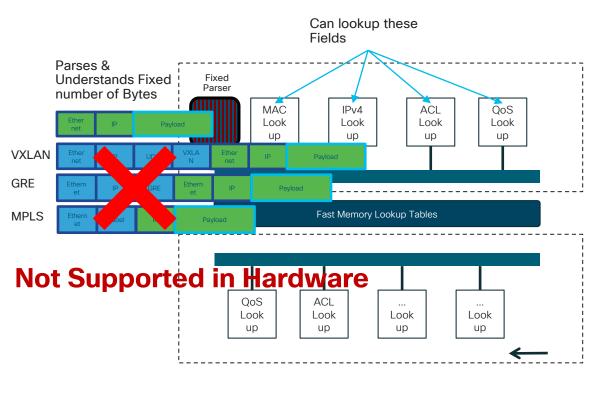
Flexible & Programmable ASIC – Adapts to the New Technologies

Traditional Networking ASICs - Fixed Pipelines



Traditional

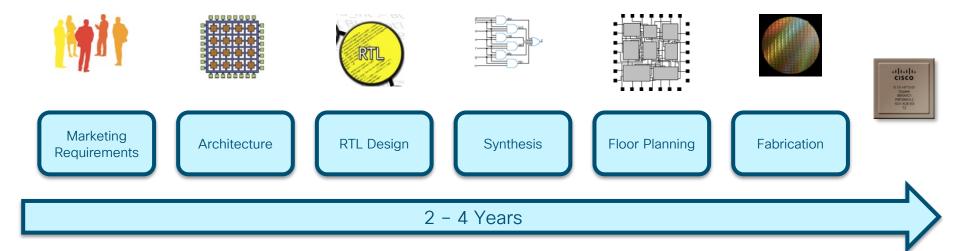
ASIC



Fixed Pipeline

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New ASICs for New Technology ?



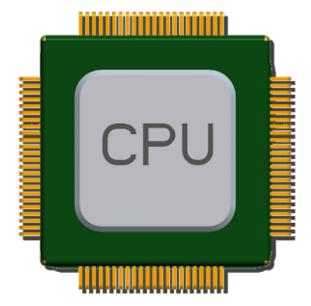
Building a new ASIC takes a lot of time & money

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How about CPUs ?



CPUs are highly Programmable

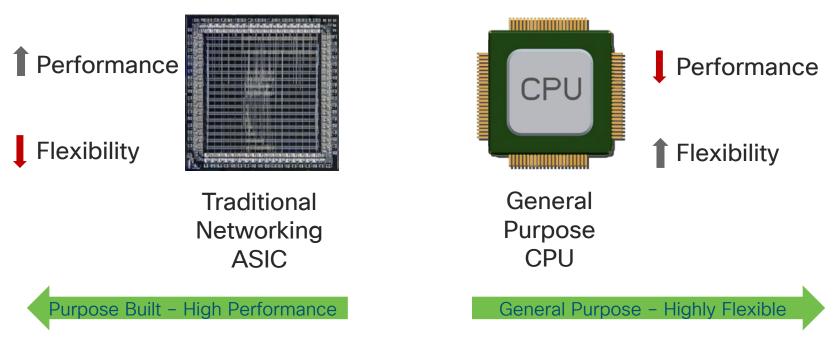




CPUs are not as fast

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Traditional Networking ASICs vs CPUs



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Cisco Innovation – UADP ASIC

In 2013 Cisco Introduced UADP (Unified Access Data Plane)



UADP brings Flexibility without compromise on Performance

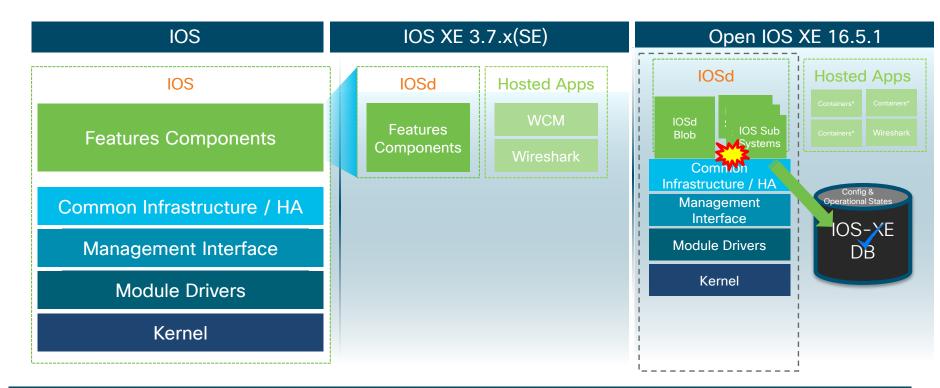
Proven Investment Protection with UADP 1.0



UADP 2.0 extends it to the Catalyst 9K Family

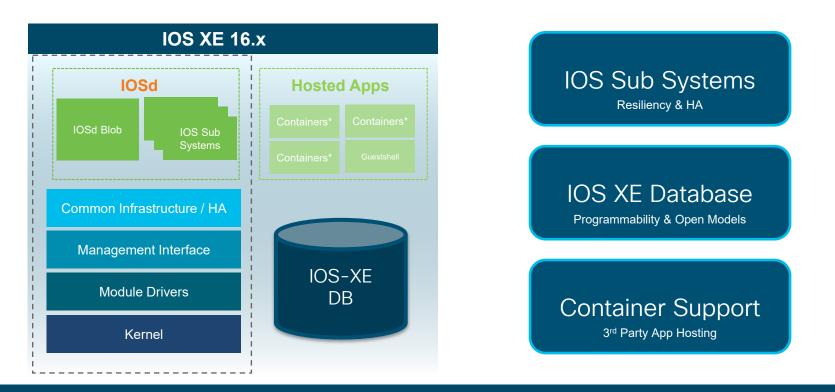
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Open IOS-XE



Modern Software Architecture with the same look & Feel

Open IOS XE – A Modern Operating System

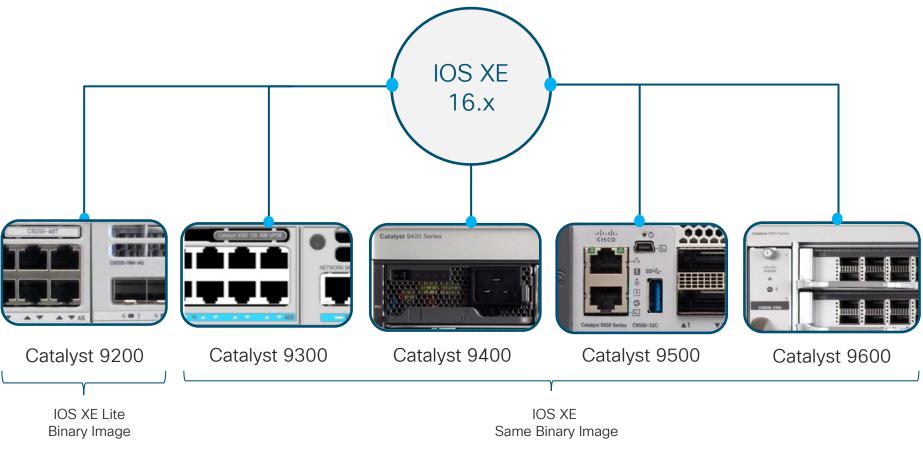


Open, Model Driven & Secure Operating System

IOS XE – Same Software on all 9K Platforms



Single Binary for the entire Catalyst 9K Family



Catalyst 9000 runs the same Operating System

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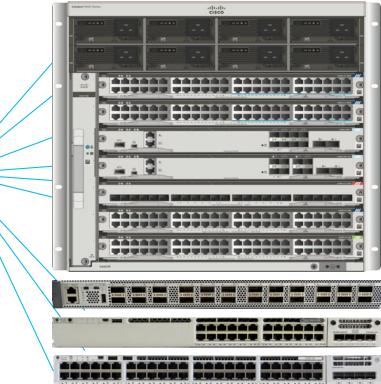
User Centric Design of Catalyst 9K





Catalyst 9K Family – Blue Beacon





Identification of Devices has never been Easier

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Catalyst 9K Family with Built-in RFID





Inventory Management (Tracking) has never been Easier

Catalyst 9K Family – Optional Bluetooth





Accessing the Device has never been Easier

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Catalyst 9400/9600 - Cool Fan Trays

Fan Trays Serviceable from Front and Back



eter Temprature Sensors

Variable Speed Fans



Flexibility in Cabling & Maintenance

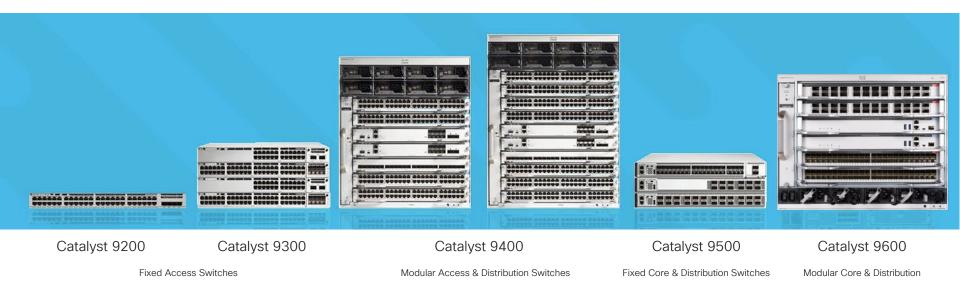
CISCO

Catalyst 9K Family – Industrial Design & Ergonomics



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The Catalyst 9K Family

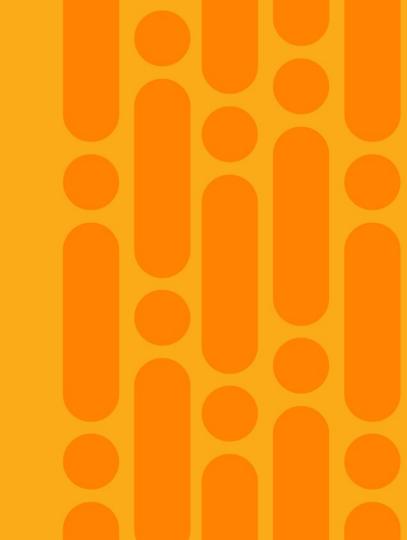


Built on Cisco's Innovative Hardware & Open IOS-XE

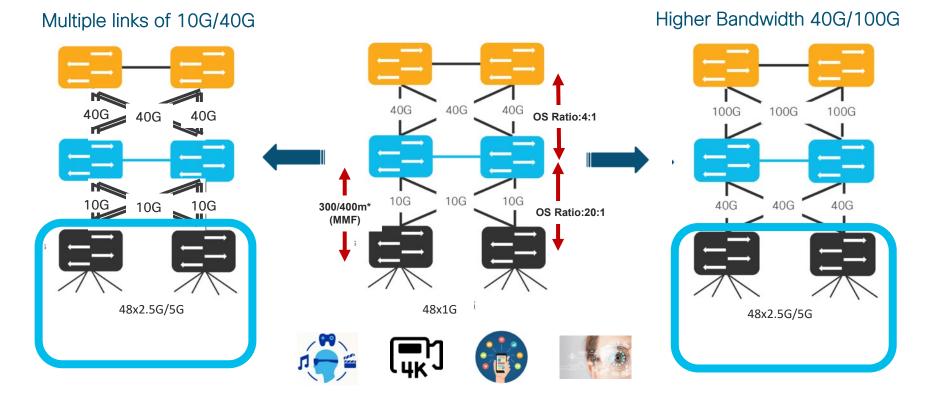
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Connectivity & Speed





New Technologies Push for New Speeds



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MultiGigabit Portfolio across the Catalyst 9000



C9300 Module: 4-port Full Multigigabit Ethernet



C9300-24UX: 24-port Full Multigigabit Ethernet



C9300-48UN: 48-port 5 Gigabit Ethernet



C9300-48UXM: 48-port 2.5 Gigabit Ethernet (12 full mGig)

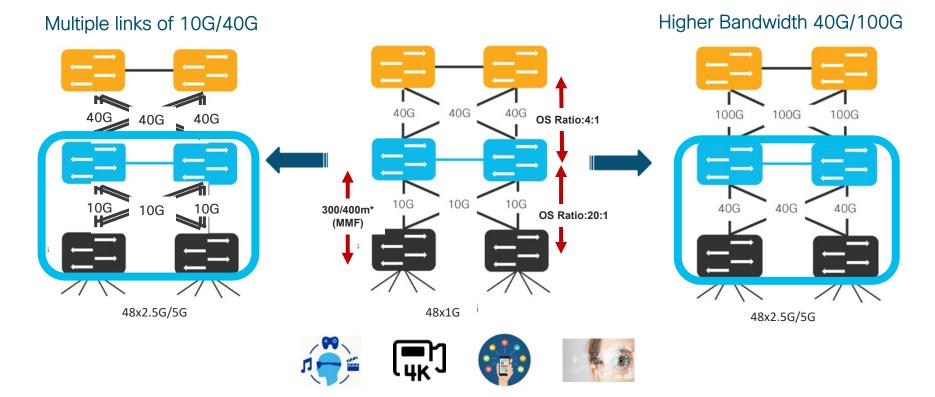
Get ready for 11ax and beyond





C9400-LC-48UX: 24-port Full Multigigabit Ethernet

New Technologies Push for New Speeds



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Challenges with 10G to 40G Migration

MPO Assemblies for Short Reach 40G



- Significant costs (transceivers and cables)
- 10G Backward Compability with 1G
- 10G X2/XFP presented a form factor conundrum
- SFP+ backward compatibility with 1G SFP and 100-Mbps SFP has enabled that speed transition
- Single-lane serial optics, providing a port density similar to that of 10G switches

Lower Port Densities

(H x W x D) 13.5 x 18.4 x 72.4 mm

(H x W x D): 8.5 x 13.4 x 56.5mm

The Challenges Slow down 40G Adoption

QSFP+

SFP+

25GE - A better alternative



25G derived from 100G

IEEE 25G standards

Provides seamless migration path from 10GE

Project	Interfaces	Description
IEEE P802.3by	25GBASE-CR	Passive copper cables up to 5 m
	25GBASE-SR	Short reach over MMF (OM3/OM4)
IEEE P802.3cc	25GBASE-LR	Long reach 10 km over SMF
	25GBASE-ER	Long reach 40 km over SMF

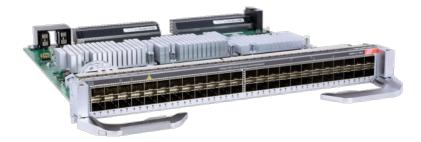
Reduced CAPEX through reuse of existing cabling

Single Lane serial optics providing port densities similar to 10G Switches

Reduced OpEx through savings in power and cooling

Cisco's 25G solution provides Speed w/o Compromise on Distance

25G Portfolio





Catalyst 9600 Line Card 48x25G

> Catalyst 9500 48x25G or 24x25G

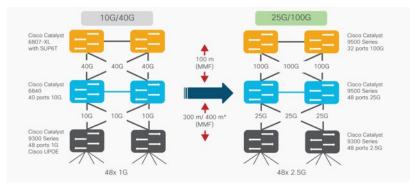


Catalyst 9400 Supervisor Module 2x25G

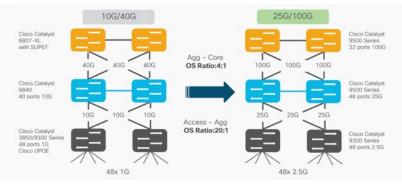


Catalyst 9300 Uplink Module 2x25G

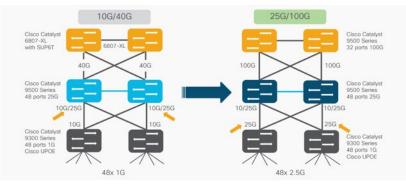
Enabling Architectural Transformation



Campus-optimized distance with Speed transition



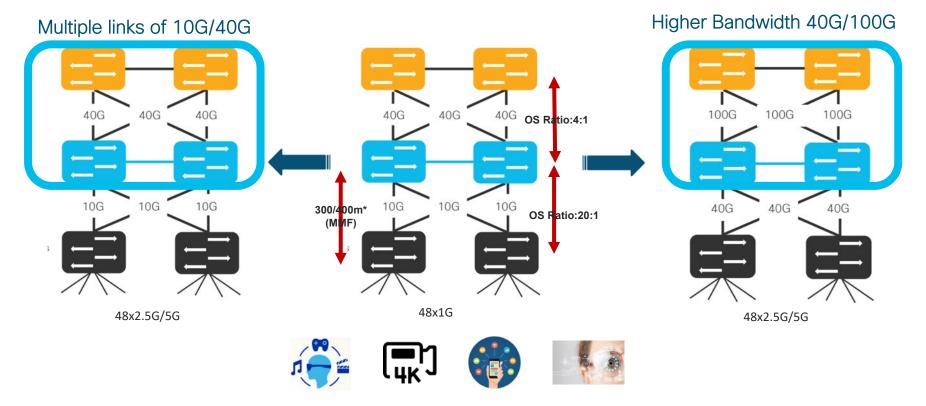
Speed transition with similar oversubscription ratio



Speed migration with dual-rate optics

And more...

New Technologies Push for New Speeds in Core



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100GE – 100G supports multiple speeds



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100G Portfolio



Catalyst 9500 32x100G, 16x100G

Catalyst 9600 48x100G

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Catalyst 9K Family Members

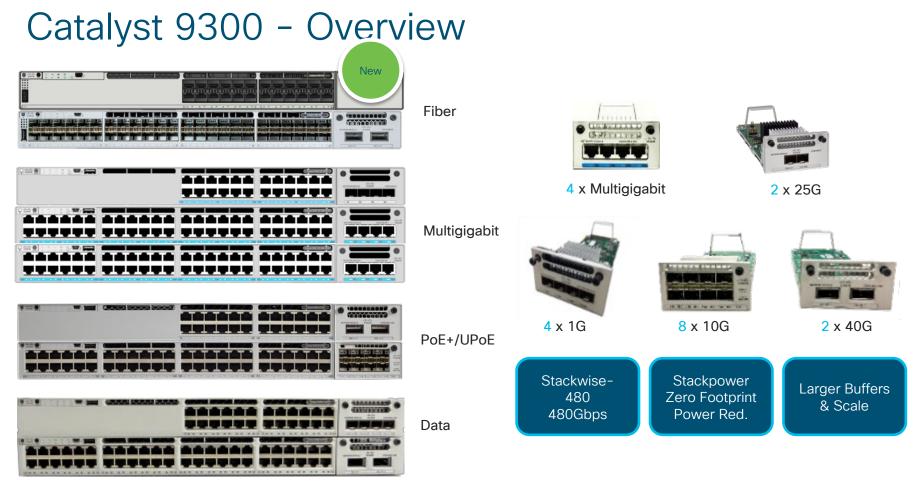




Stackable Access



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Catalyst 9300L

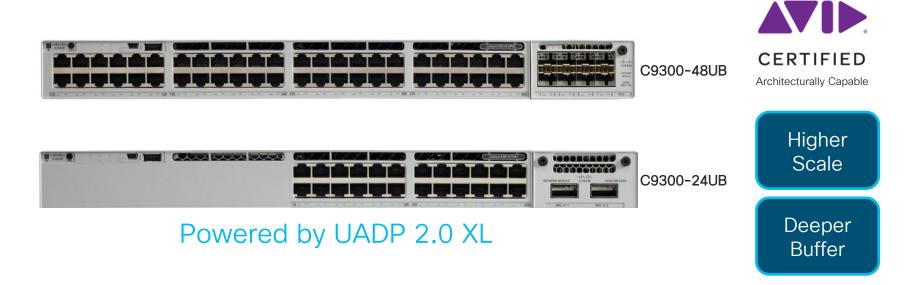
New Shipping





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Catalyst 9300-B Increased Scale Platform



Stackable Access Optimized for Media Distribution & IP Storage

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New

Catalyst 9300-B Lookup Tables

	Cisco® Catalyst® 9300-B Series Advantage	Cisco [®] Catalyst [®] 9300-B Series Essentials
MAC addresses	64,000	32,000
Host/Direct routes	48,000	24,000
IGMP groups	16,000	8000
LPM/Indirect routes	64,000	8000
Multicast routes	16,000	8000
SGTs	8000	8000

	Cisco [®] Catalyst [®] 9300-B Series Advantage	Cisco® Catalyst® 9300-B Series Essentials
Security ACL entries	18,000	5000
• PACL		
• VACL		
• RACL		
QoS ACL entries	18,000	5000

NetFlow entries: 128,000/64,000 per UADP 2.0 XL/2.0 ASIC

Higher Scale with Advantage License

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NetFlow

Cisco Catalyst 9300 – IEEE 802.3bt Compliance Introducing 90W UPOE+ Models to power latest intelligent devices

~ 21 Ports of 90W in Standalone Mode



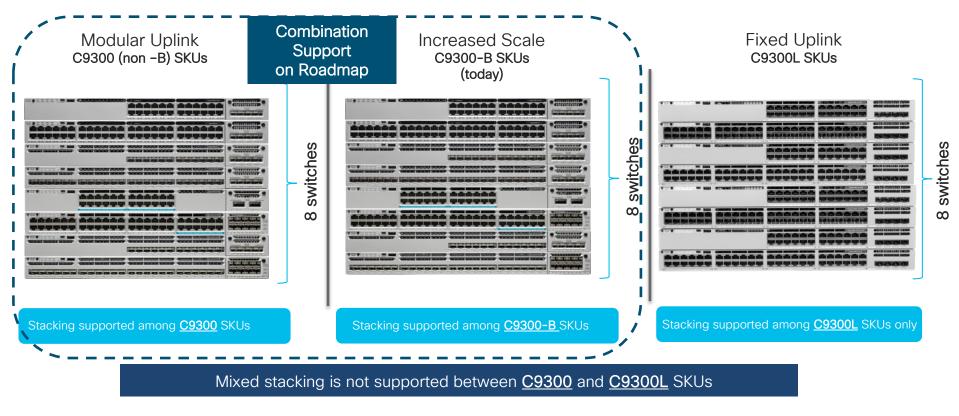
168 Ports of 90W in StackWise-480



Highest 90W Port Density in the industry

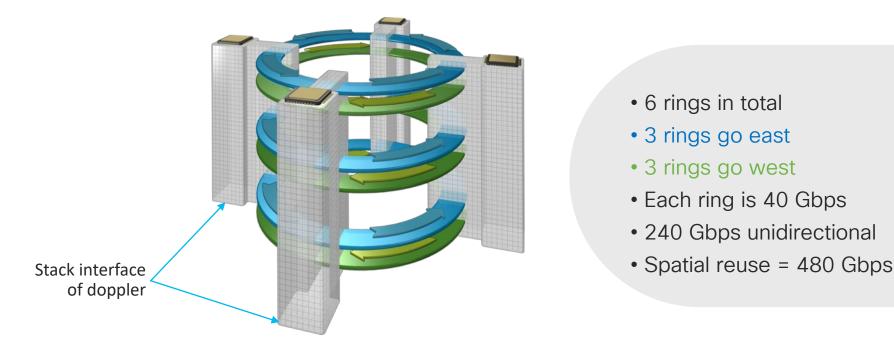
Feb `20

Catalyst 9300 Stacking Support



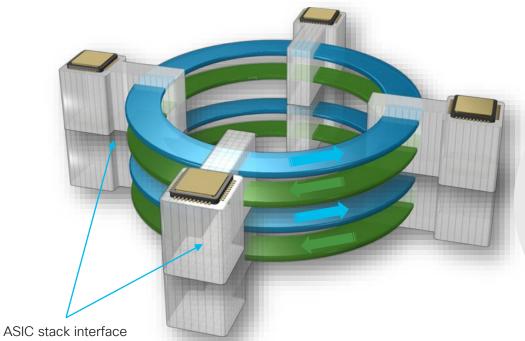
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Cisco Catalyst 9300 Series Switches The stack ring – StackWise-480 on C9300 SKUs



Assuming 4x 24-port Cisco[®] Catalyst[®] 9300 Series modular uplink models

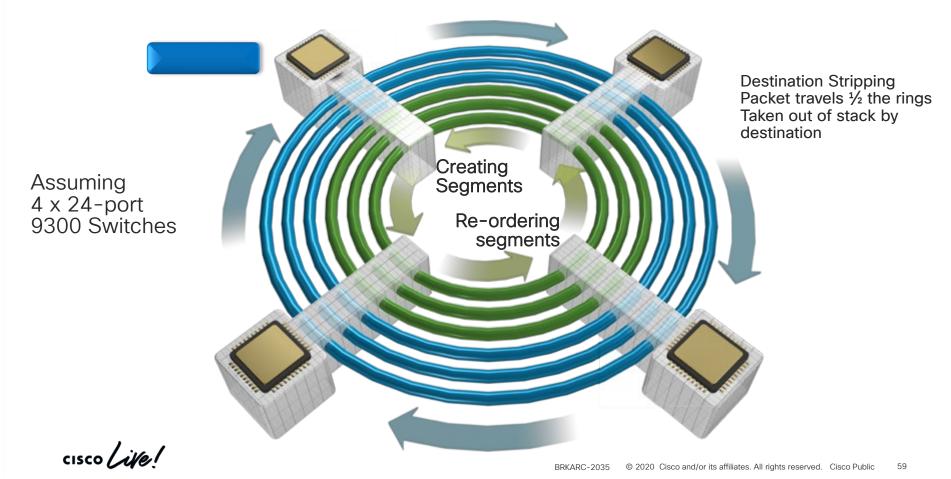
Cisco Catalyst 9300 Series Switches The stack ring – StackWise-320 on C9300L SKUs



- 4 rings in total
- 2 rings go east
- 2 rings go west
- Each ring is 40 Gbps
- 160 Gbps bidirectional
- Spatial reuse = 320 Gbps

Assuming 4x 24-port Cisco® Catalyst® 9300 Series fixed uplink models

Unicast Packet Path



StackPower - overview

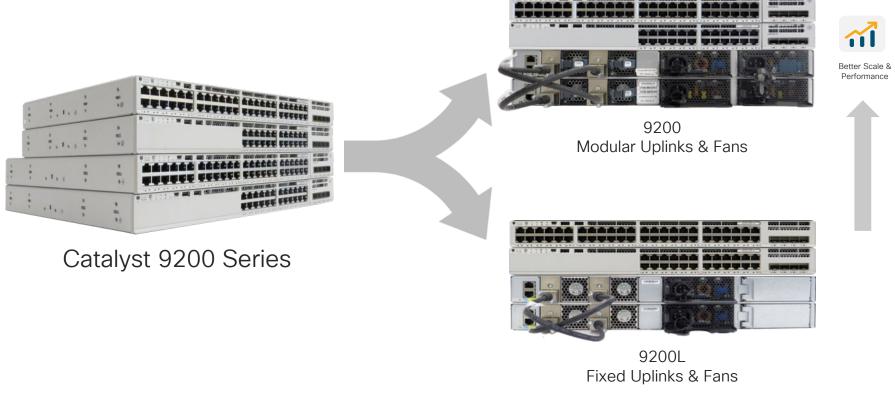
"Zero-footprint" redundant power system (RPS) deployment



- Provides RPS functionality with zero RPS footprint
- Pay-as-you-grow architecture similar to the data stack
- 1+N redundancy with inline power
- Up to 4 switches in a StackPower ring
- Multiple StackPower possible within one data stack
- Up to 8 switches in a star topology with an expandable power system (XPS)

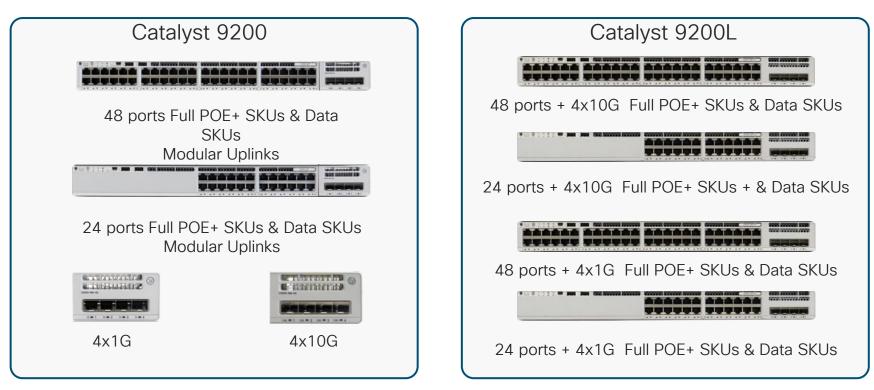
StackPower is not supported on C9300L SKUs

Catalyst 9200 - Fixed and Modular



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Catalyst 9200 - SKUs Summary

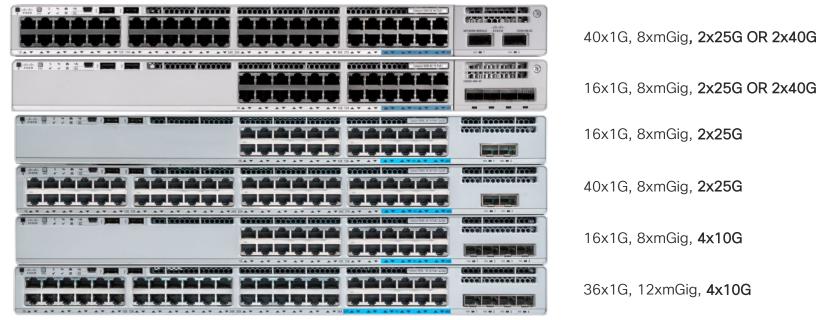


Optional Second Power Supply available for All SKUs

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The Latest Addition to Catalyst 9200 Family

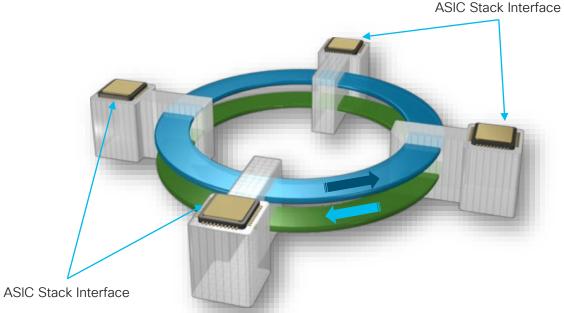




Catalyst 9200 MultiGigabit Portfolio

Get ready for 11ax and beyond

The stack ring – StackWise 160/80



- 2 rings in total
- 1 ring goes East
- 1 ring goes West
- Each ring is 40/20 Gbps ٠
- 80/40 Gbps bi-direction
- Spatial Reuse= 160/80 Gbps

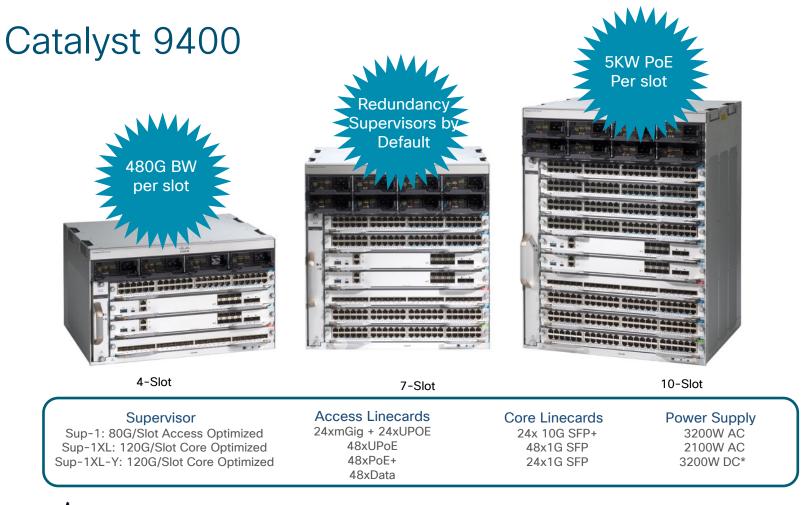
Assuming 4 x 24-port Catalyst 9200 Series modular switches

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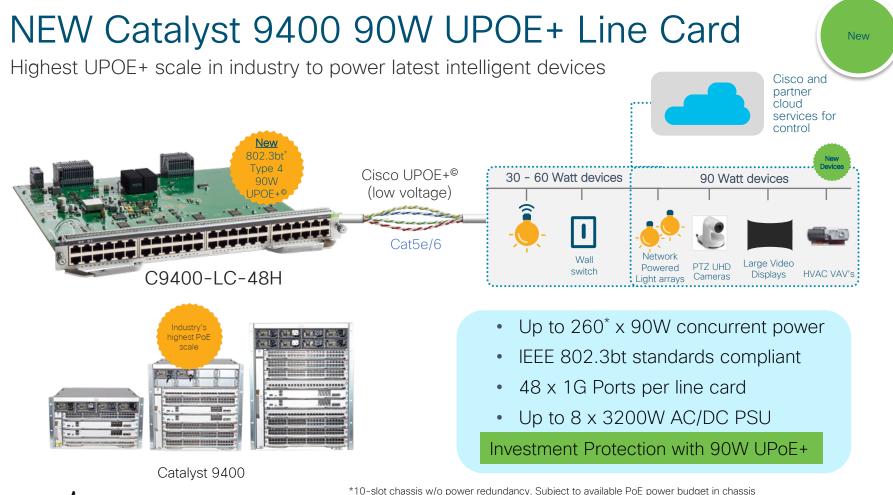
Catalyst 9400



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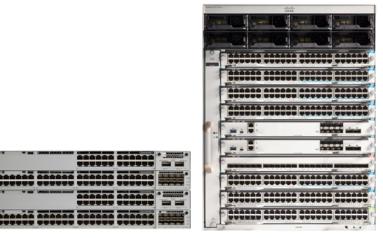


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10000 W/O power redundancy. Oubject to available rice power badget in chaosio

Catalyst 9300/9400 vs 9200





Simplicity without Compromise

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	*****	*****		
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				BOR HORSEN IN
		*****	*****	-
**************************************	NAME AND ADDRESS OFFICE	PROPERTY ADDRESS ADDRESS	CONTRACT OF CONTRACT	Will Strength of
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Catalyst 9300/9400



Options for Different Deployments and Use Cases

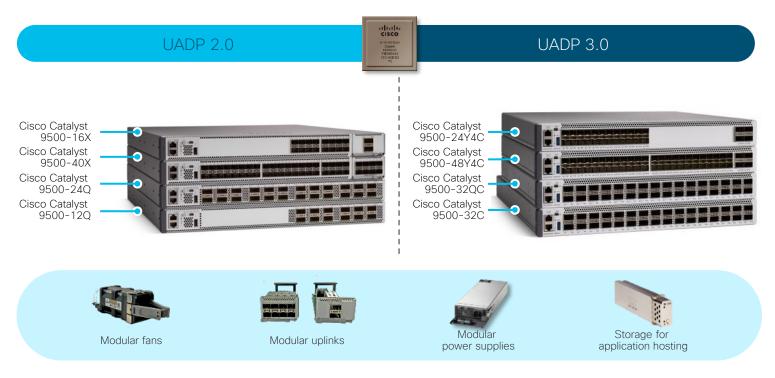
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Catalyst 9500



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Catalyst 9500 Series



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Catalyst 9600



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Cisco Catalyst 9600 Series Chassis



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Cisco Catalyst 9600 Series C9606R chassis port density



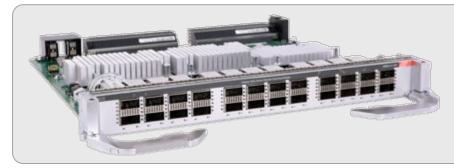
Port speed	Density with supervisor 1	Maximum chassis density
100G	48	128
40G	96	128
25G	192	192
10G	192	192
1G*	192	192

Line Rate non-blocking

*Roadmap

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Cisco Catalyst 9600 Series Line cards



C9600-LC-24C - 100G/40G (fiber)

- 24 ports
- QSFP28/QSFP+
- Supports 100G and 40G

C9600-LC-48YL - 25G/10G/1G* (fiber)

- 48 ports
- SFP28/SFP+/SFP
- Supports 25G, 10G, and 1G



*Roadmap The Y in the product ID (PID) indicates the hardware capability

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Cisco Catalyst 9600 Series mGig Line Card - C9600-LC-48TX

For collapsed core deployment and connectivity to application servers



- All 48 ports can support 10G/5G/2.5G/1G/100M/10M
- Line rate on all ports. Any port, any supported speed.
- No PoE
- Port reference is "Ten<slot#>/0/<port#>" and port speed is auto (default).

Cisco Catalyst 9600 Series Fan tray



Fan tray hot-swappable needs to be done within 120 seconds

Cisco Catalyst 9600 Series Power supplies



- Chassis has 4 slots for power supply
- Individual on/off switch for each power supply
- Supports a mix of AC (@220V) and DC power supplies

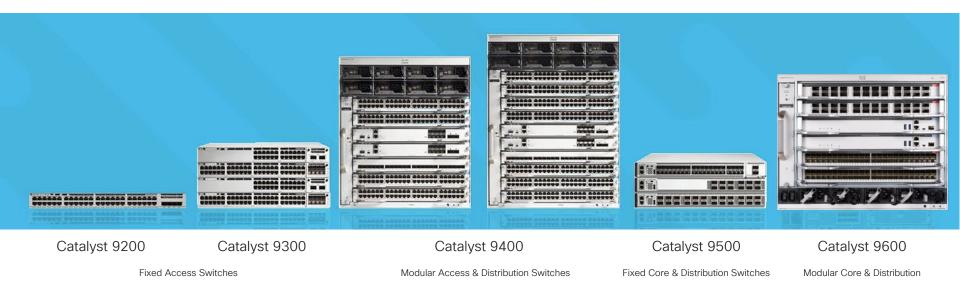


- Supports both 110V and 220V input
- 2 KW output with 220V (1050W with 110V)
- Platinum rate power supply
- Redundant mode: Combined and N+1



- Supports input range of -40V to -72V
- 2 KW output
- Platinum rate power supply
- Redundant mode: Combined and N+1

The Catalyst 9K Family



Built on Cisco's Innovative Hardware & Open IOS-XE

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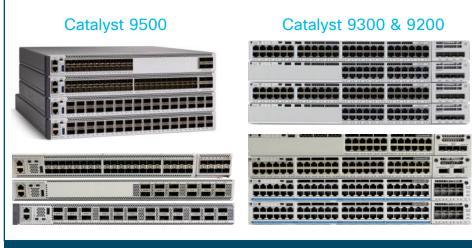
An Architectural View

Platform Architecture & Layouts

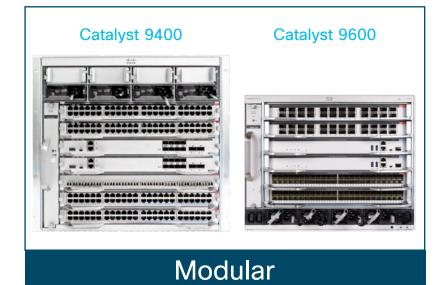




The Catalyst 9K – Fixed & Modular Platforms

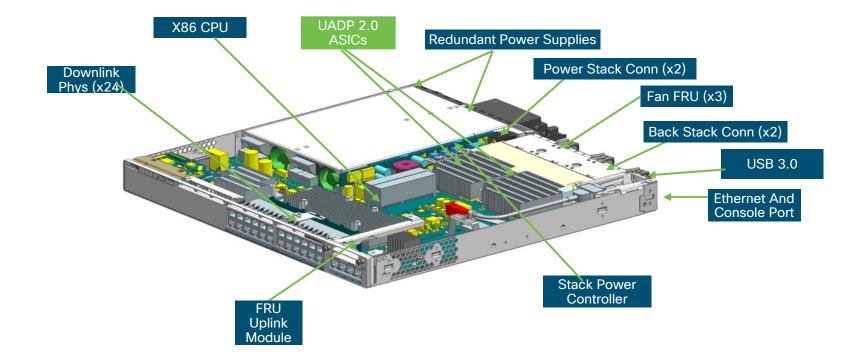


Fixed

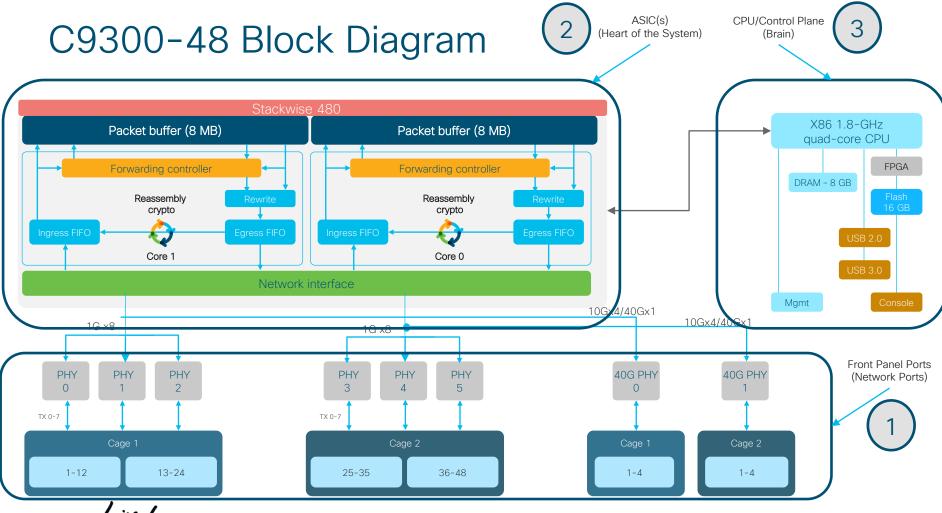


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Fixed Architecture



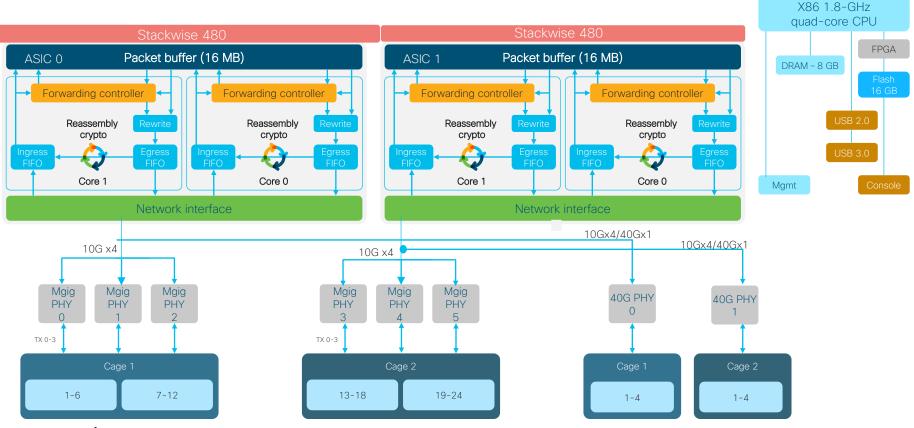
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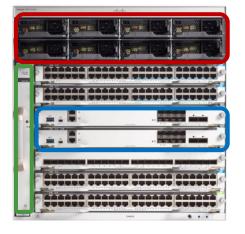
C9300 Multigigabit-24 Block Diagram



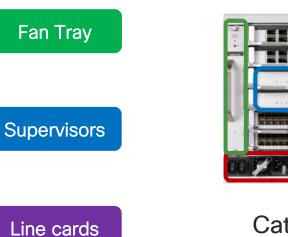
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Catalyst 9400 - Modular

Power Supplies



Catalyst 9400



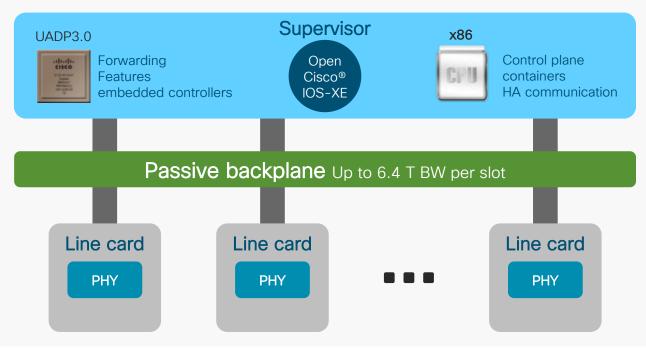


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Catalyst 9600 - Centralized Architecture

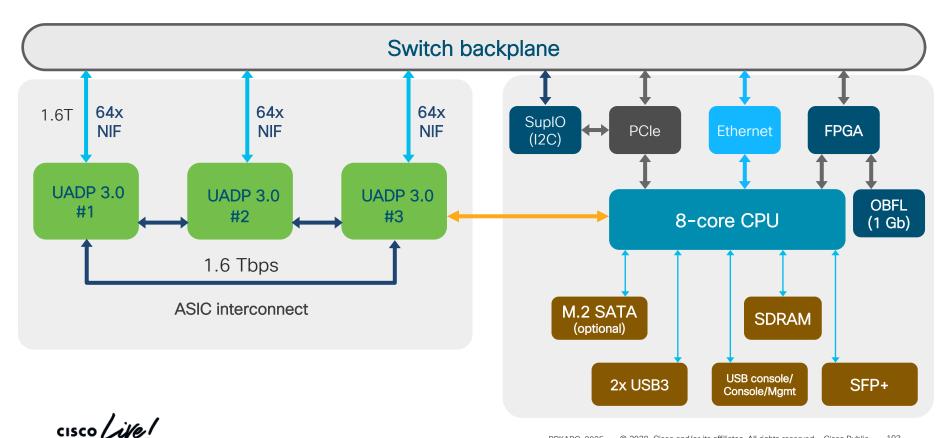
Centralized architecture



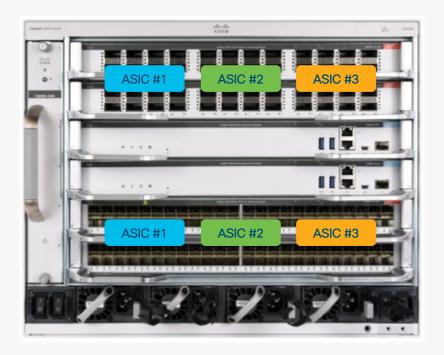
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- Centralized architecture => Uninterrupted supervisor switchover
- Centralized architecture (Forwarding, queuing, and security are done on the supervisor) => Unlock new capability with a supervisor upgrade
- Transparent line cards =>
 Compatible with new sup
- Passive backplane => High MTBF
- X86 CPU + storage => App hosting

Catalyst 9600 - Supervisor 1 - Block diagram



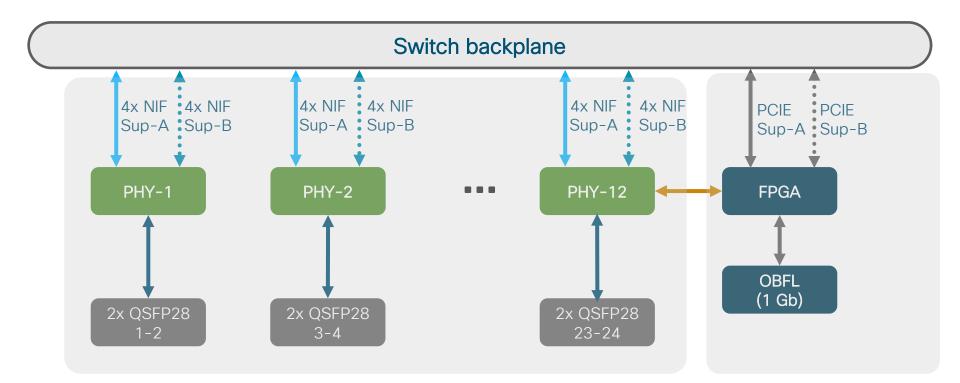
Supervisor engine 1 – ASICs to LC mapping



- ASIC #1: First third of the ports
 - 48-port module: 1-16
 - 24-port module: 1-8
- ASIC #2: Middle third of the ports
 - 48-port module: 17-32
 - 24-port module: 9-16
- ASIC #3: Last third of the ports
 - 48-port module: 33-48
 - 24-port module: 17-24

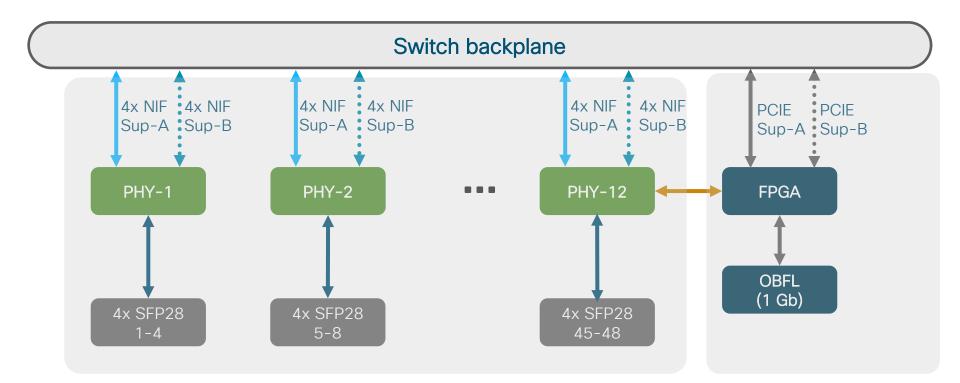
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100G/40G line card block diagram



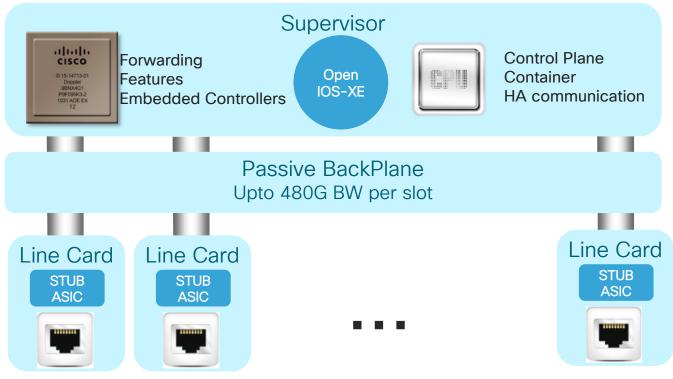
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25G/10G/1G line card block diagram

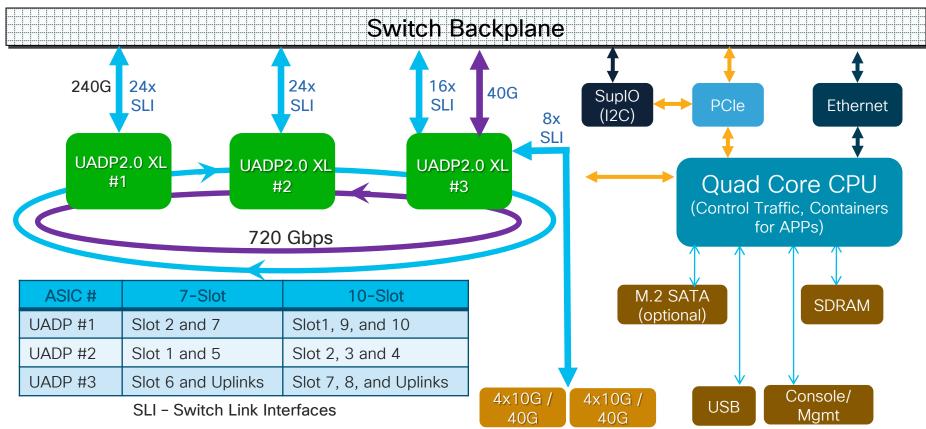


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Catalyst 9400 - Centralized Architecture

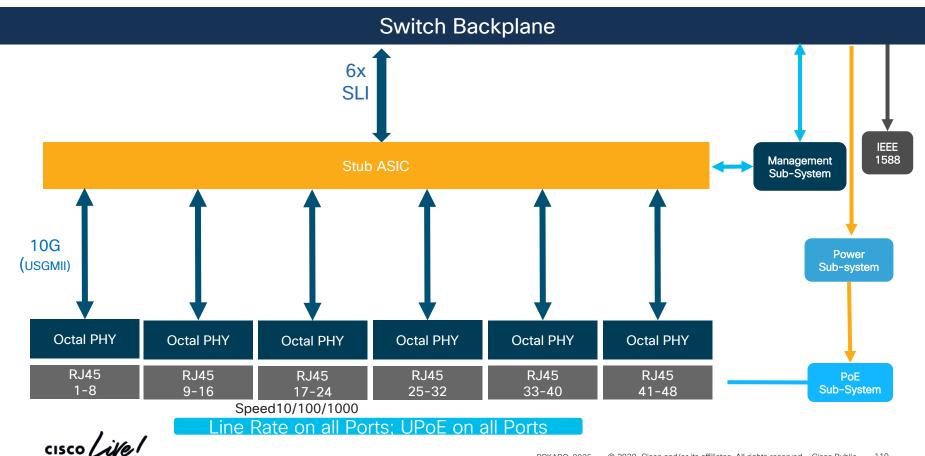


Catalyst 9400 - Sup-1/Sup-1XL Block Diagram

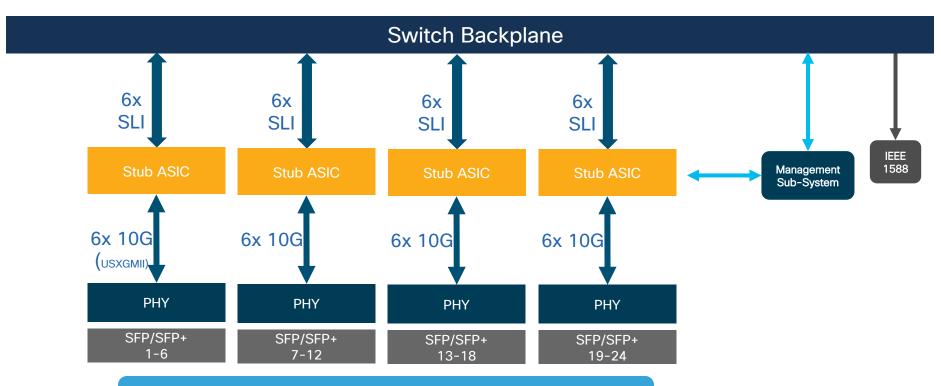


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Catalyst 9400 - 48x1G RJ45 Line Card (UPoE)



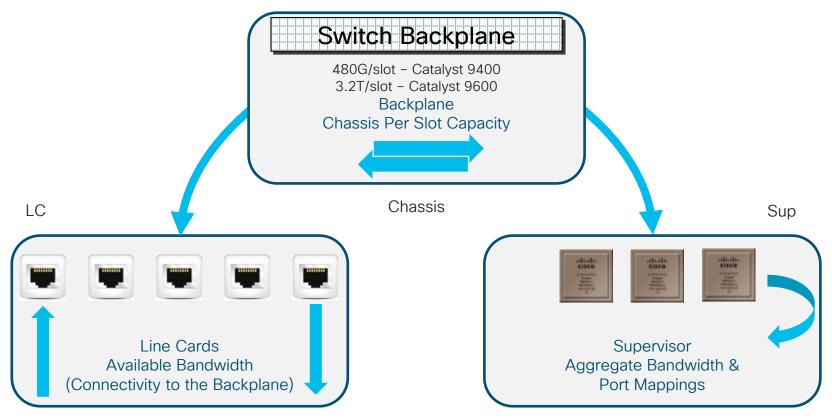
Catalyst 9400 - 24x 1/10G SFP/SFP+ Line Card



Sup-1: 80G with C9410R and C9407R Sup-1XL: 80G with C9410R; 120G with C9407R

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Modular Architecture – Key Components



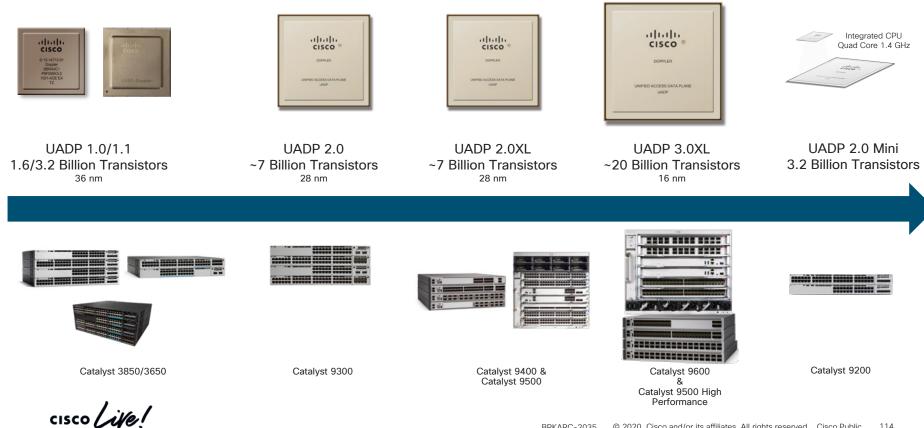
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UADP Family – A Programmable ASIC

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UADP Evolution

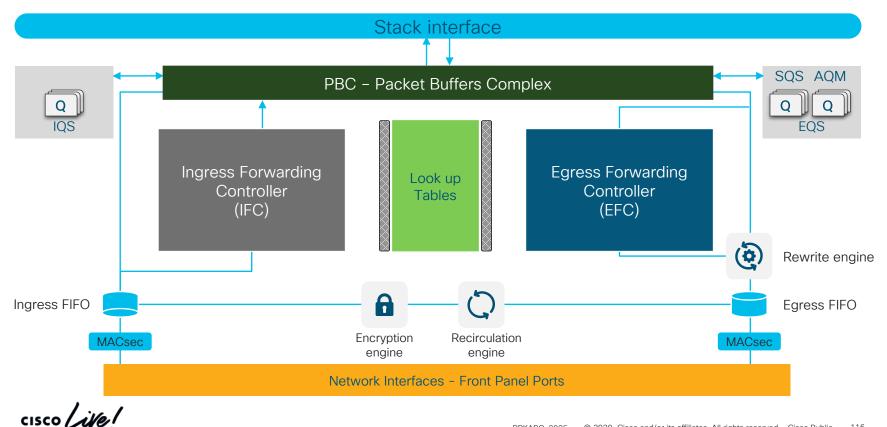


UADP Family for Catalyst 9K

	UADP 2.0 mini	UADP 2.0	UADP 2.0 XL	UADP 3.0
Total Bandwidth	100G	160G	240G	1.6T
TCAM Entries	8K	20К	54K	54K
Buffers	6MB	16MB	32 MB	36 MB
Stack Bandwidth	160G	240G	720G	800G/1.6T
Stack Ring	1	1	2	2

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UADP Core Architecture



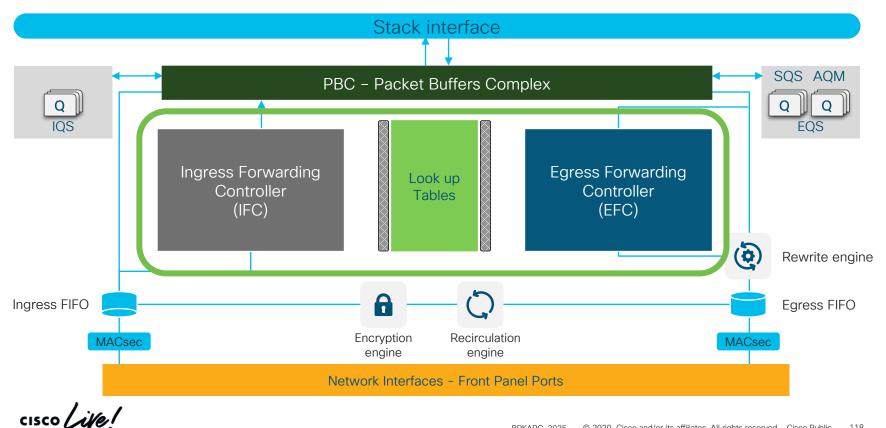
Some of the Key Capabilities of UADP ASIC



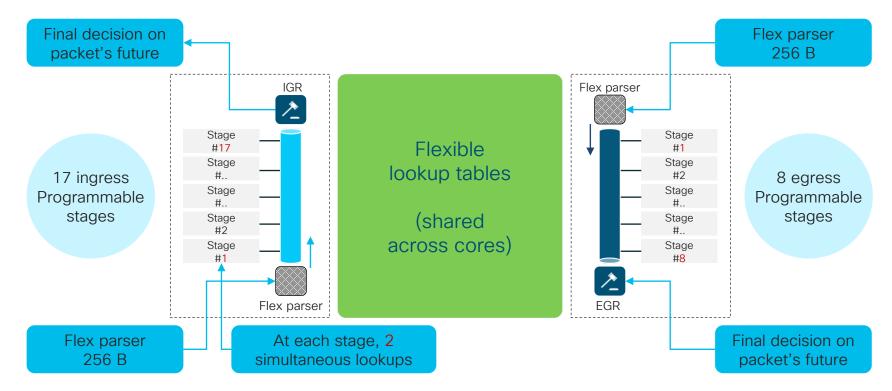
No Compromise on Performance

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UADP – Programmable Pipeline

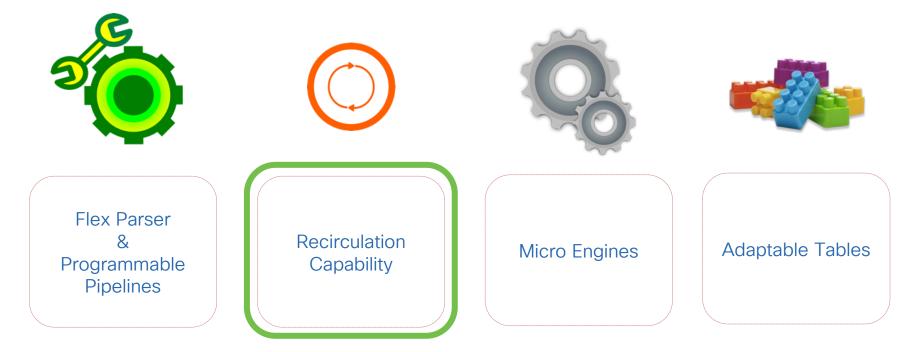


Programmable Pipelines – Closer Look



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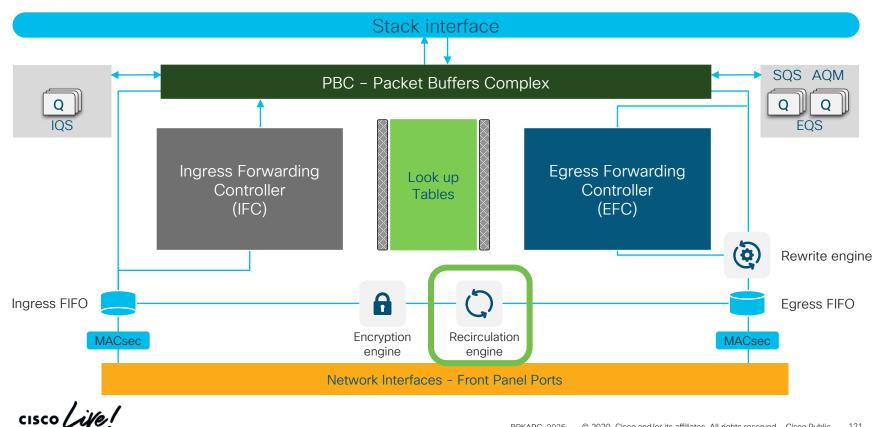
Some of the Key Capabilities of UADP ASIC



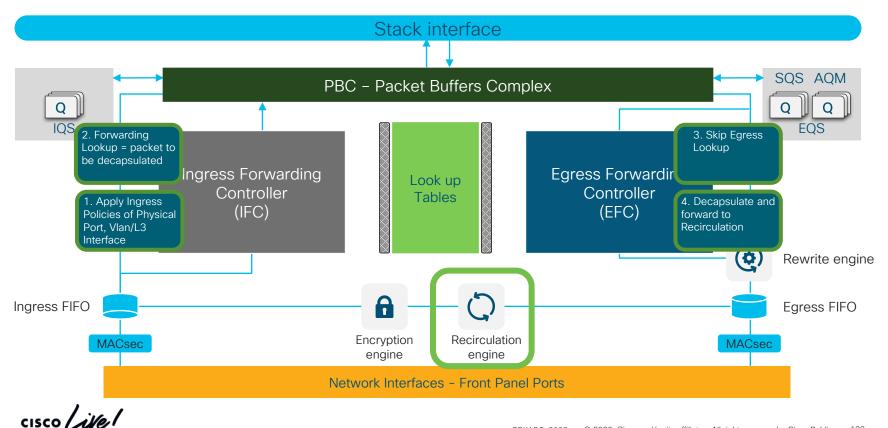
No Compromise on Performance

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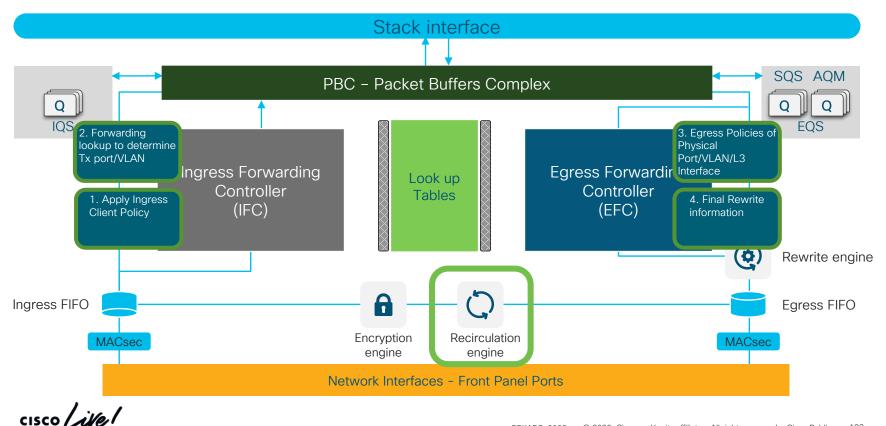
UADP – Recirculation Engine



UADP - Recirculation First Pass



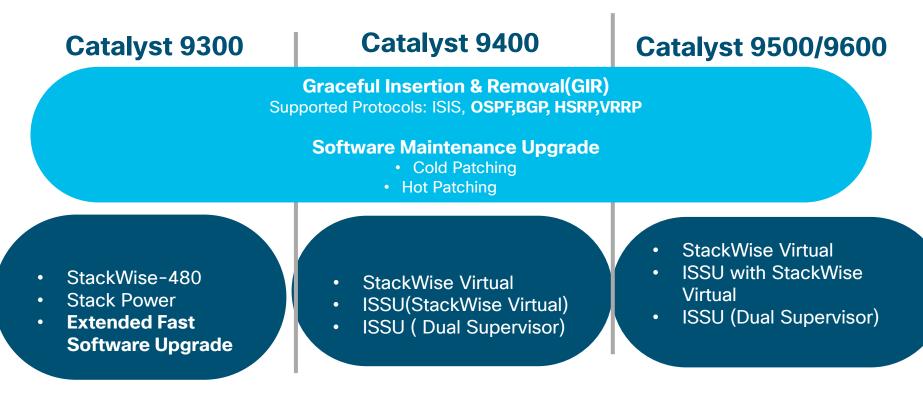
UADP – Recirculation Second Pass



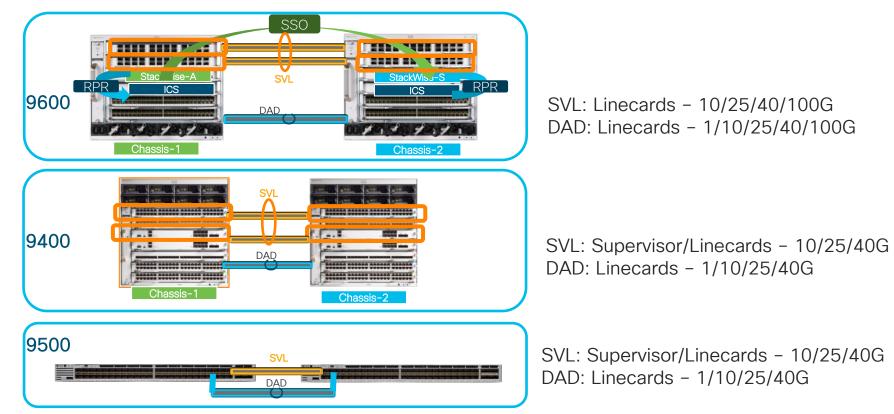
High Availability with Catalyst 9000



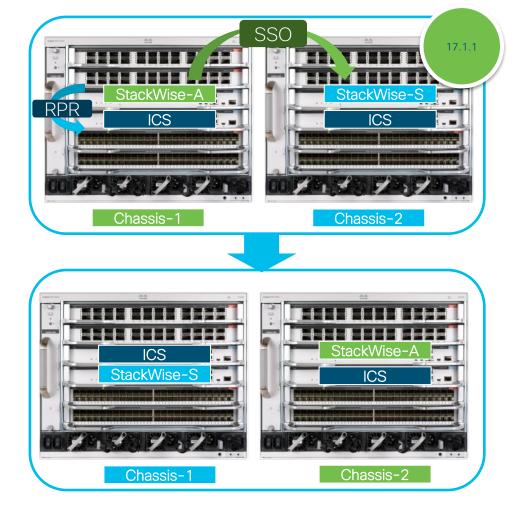
High Availability with Catalyst 9000



StackWise Virtual Platforms - Deployment



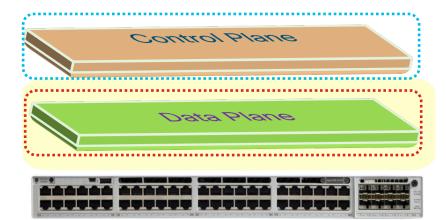
Quad Sup **StackWise** Virtual



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Extended Fast Software Upgrade on Catalyst 9300

- xFSU provides a mechanism to independently update the control plane and data plane during the upgrade process
- Control plane is upgraded by leveraging <u>Graceful Reload</u> <u>Infrastructure</u> without impacting data plane traffic
- Data plane(ASIC) is re-programmed in less than 30 seconds by leveraging <u>special cache memory</u> which stores active forwarding entries



Extended Fast Software Upgrade



9300 Standalone

#Install add file image activate reloadfast commit



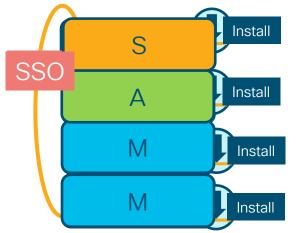
9300 Stack

#Install add file image activate reloadfast commit

Control Plane

Fast Software Upgrade on Stack

#Install add file image activate reloadfast commit



- 1. Install the images on all switches
- 2. Fast reload the standby and member switches
- 3. Fast reload the active switch only
- 4. Standby becomes the new active
- 5. Old Active switch becomes the new standby

Traffic Impact during the complete upgrade is less than 30 seconds

Closing & Wrap





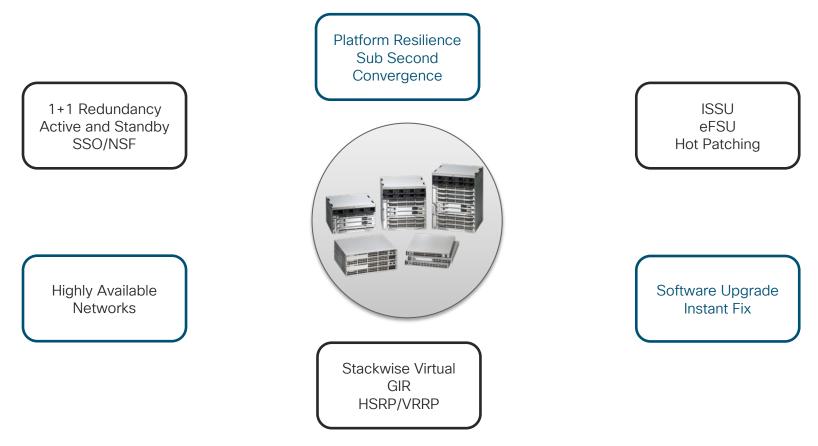
Catalyst 9K has fundamentally changed the Networks

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Security		Secure Infrastructure		Secure Transport	
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Umbrella Integration		Trustworthy Systems		MACSEC	
Tra Anal		Hardware Authenticity		Man-i the-Mic	
Malware Detection	Compliance		Run-time Defense	Wire-tapping	Impersonation

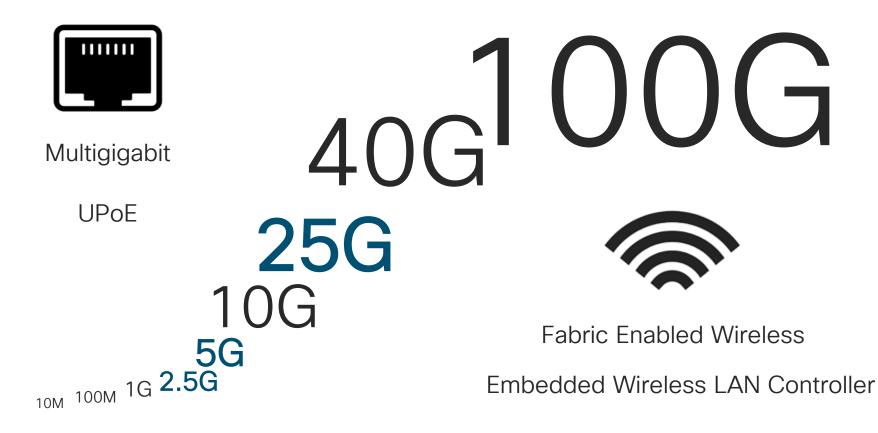
Catalyst 9K makes Our Networks more Secure than ever

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Catalyst 9K makes Our Networks most Resilient

cisco ile



Catalyst 9K makes Our Networks Ready for 11ax

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Full PoE+/UPoE

AVB & PTP

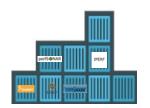
IoT Readiness



Catalyst 9K makes Our Networks Ready for IoT

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Application Hosting & Dockers



Flexibility and Control

Network Analytics Monitoring Tools Tools

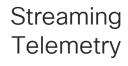
Model Based API & Programmability

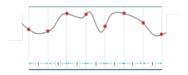


Network Automation

Candidate Datastore

Build Your Own





Monitoring the Network



Catalyst 9K makes Our Networks Programmable

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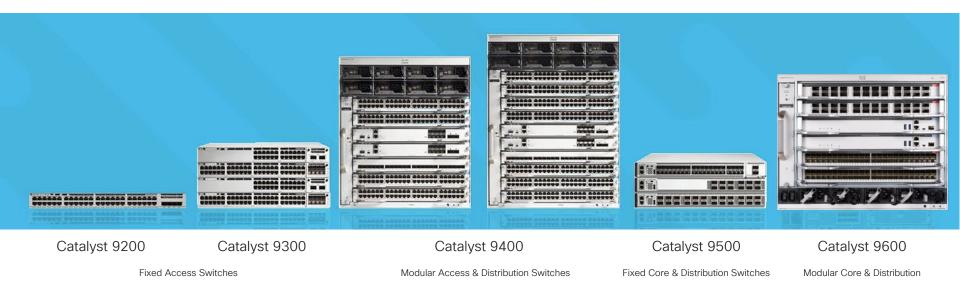
Software Defined Access

Cisco DNA Assurance

Catalyst 9K makes Our Networks Software Defined

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The Catalyst 9K Family



Built on Cisco's Innovative Hardware & Open IOS-XE

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Industry Recognitions...



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Cisco Catalyst 9000 Series Switches: Independent Feature Validation

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Cisco Catalyst 9500 Series Performance Validation

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5 October 2018 DR1807111

Miercom.com



Catalyst 9K Book

2nd Edition

Cisco Catalyst 9000 A New Era of Networking

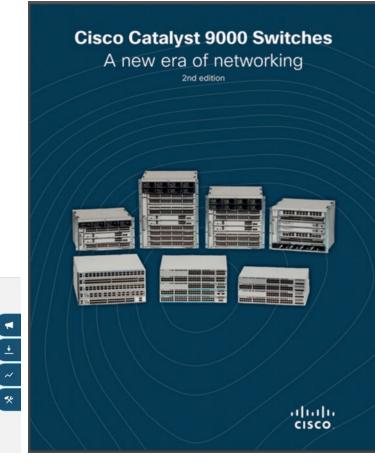
eBook Available on Cisco.com



Catalyst 9000 deep dive

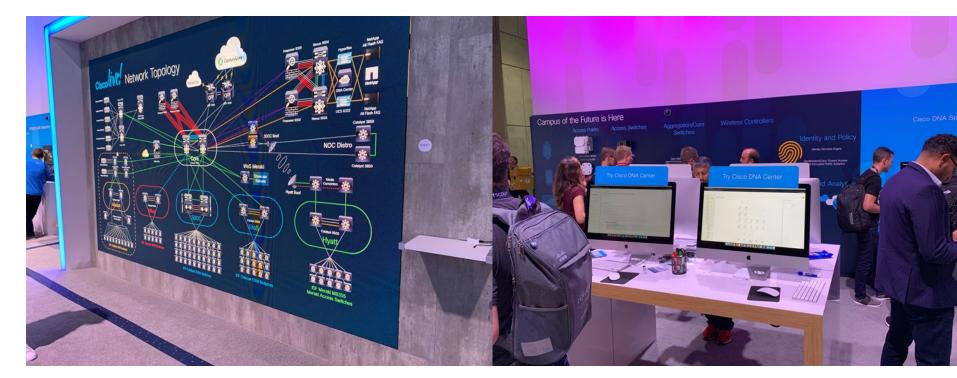
Learn how the Catalyst 9000 family of switches helps you address your top IT challenges, including security, high availability, quality of service, and more.

Read the e-book



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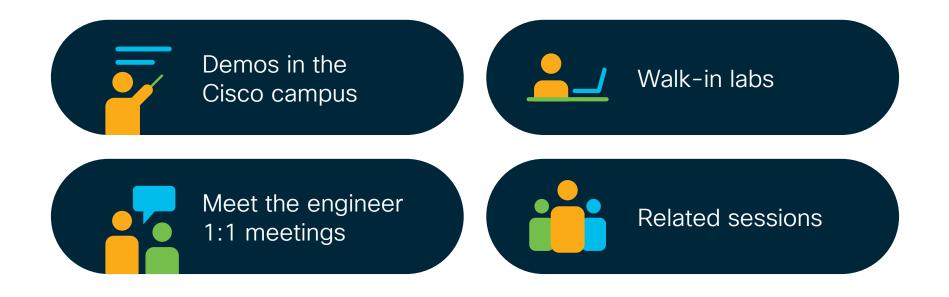
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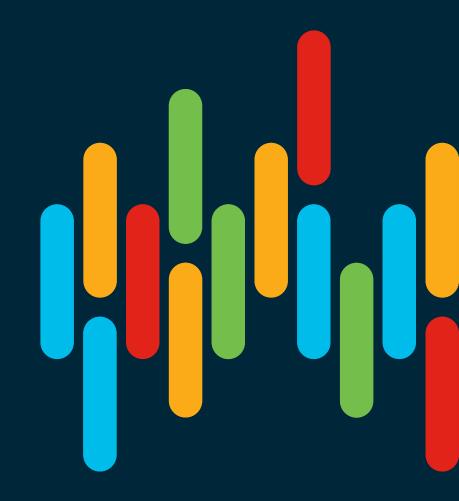


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Thank you



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You make **possible**